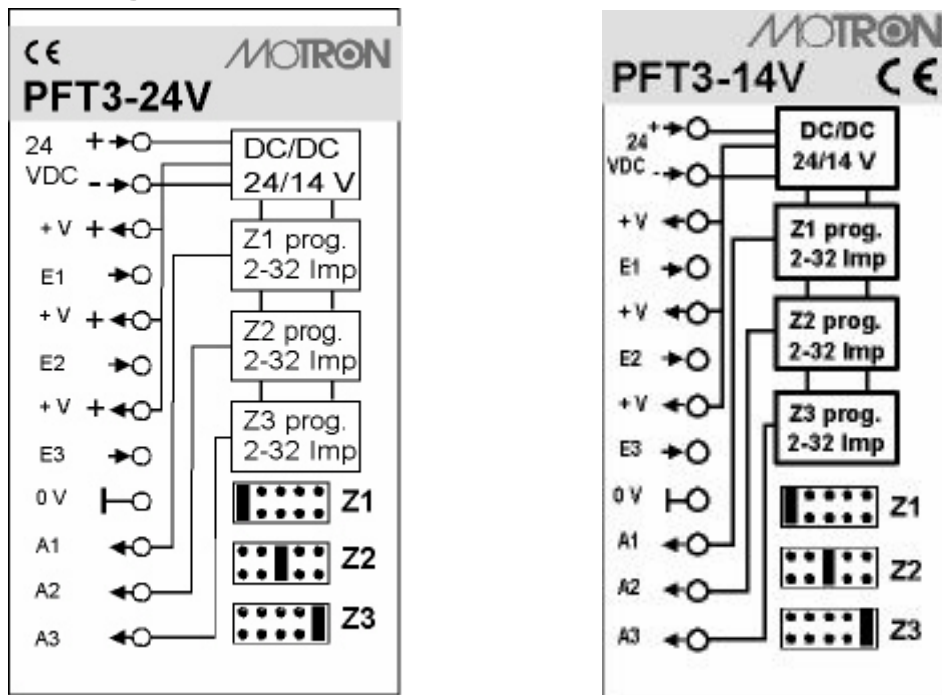


Programmable frequency divider PFT3



The PFT3 includes three independent channels Z1, Z2 und Z3.

The internal operating voltage is generated with a switching power supply. This power supply is short circuit protected. An external clock is supplied by this 14-V-voltage.

The inputs E1, E2 and E3 are processing positive Signals up to 1 kHz.

The dividing ratio of the counters Z1, Z2 and Z3 is determined with jumpers, one per counter, left to right from 2 up to 32.

The LED's, indicating the outputs A1, A2 and A3, are protected with an serial resistor to the load.

The PFT3 is solid-state and maintenance-free.

Please keep it free from humidity and dirt.

The mounting position is variable.

Housing B x H x T	22,5 x 75,5 x 69 mm for rail mounting
Power supply	24 VDC (20 – 30 V) ca. 50 mA
Input	low ≤ 1 V, high ≥ 10 V, Re ca. 30 k Ω
Output	low ≤ 2 V, high ≥ 17 V, max. 10 mA at 24 VDC power supply